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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU  | EXAMINER                     |
|----------|-------------|-------|----------|------|------------------------------|
| 10026639 | 12/27/2001  | 257   |          | 2841 | <del>10026639</del> 10026639 |

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**\*\*CONTINUING DATA VERIFIED:**

**\*\* FOREIGN APPLICATIONS VERIFIED:**  
JAPAN 2000-402818 12/28/2000

|   |   |                                  |
|---|---|----------------------------------|
| PG-PUB  | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> |
| Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no<br>35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no<br>Verified and Acknowledged Examiners's initials |   | ATTORNEY DOCKET NO<br>217713US2  |
| TITLE : Semiconductor-chip mounting substrate and method of manufacturing the same<br><small>U.S. DEPT. OF COMM./PAT. &amp; TM-PTO-436L (Rev. 12-94)</small>  |   |                                  |

|   |           |                             |                        |
|---|-----------|-----------------------------|------------------------|
| <b>NOTICE OF ALLOWANCE MAILED</b>   |           | <b>CLAIMS ALLOWED</b>       |                        |
|   |           | Total Claims                | Print Claim for O.G.   |
| <b>ISSUE FEE</b>  |           | <b>DRAWING</b>              |                        |
| Amount Due  | Date Paid | Sheets Drwg.                | Figs. Drwg. Print Fig. |
|   |           |                             |                        |
| <input type="checkbox"/> <b>TERMINAL<br/>DISCLAIMER</b>   |           | <b>Application Examiner</b> |                        |
|   |           | <b>PREPARED FOR ISSUE</b>   |                        |
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